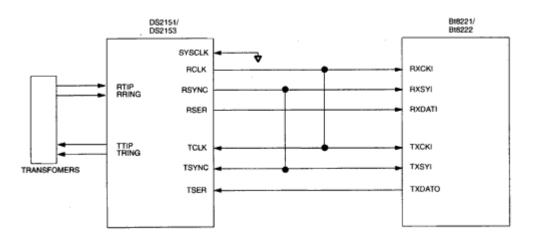


## App Note 305: DS2151, DS2153 Interfacing to the Brooktree Bt8221 and Bt8222

*This application note shows connection of the DS2151 T1 single chip transceiver and DS2153 E1 single chip transceiver to the Brooktree Bt8221 and Bt8222 (ATM Receiver/Transmitter w/UTOPIA).* 



## Notes:

- 1. Bt device is programmed for a "framed" input (i.e., Unframed Input control bit = 0).
- 2. RSYNC is programmed to output a frame sync; TSYNC is programmed to input a frame sync.
- 3. Receive and transmit electric stores are disabled.
- 4. A "loop-timed" application is shown.
- 5. Timing is shown below:

RCLK/TCLK/RXCKI/TXCKI	
RSYNC/TSYNC/RXSYI/TXSYI	
T1 Case:	LSB F M\$B
RSER/TSER/RXDATI/TXDATO	Channel 24 Channel 1
E1 Case:	LSB MSB
RSER/TSER/RXDATI/TXDATO	Terresion 31 Timesion 0